

**Amendments to the Specification**

Please replace the Abstract with the attached substitute Abstract.

Abstract

Integrated circuit comprising series-connected subassemblies

The integrated circuit comprises subassemblies connected in series enabling synchronization of the subassemblies to be achieved in simple manner. Each subassembly comprises a first and a second power supply terminal and a clock input. The subassemblies are connected in series to the terminals of a voltage supply source so that the same current flows through the different subassemblies. The clock input of each subassembly is connected to a common clock circuit by means of devices for shifting the clock signal levels, for example comprising capacitors and/or transistors. Each subassembly can comprise a decoupling capacitor and a voltage limiting circuit between its first and second power supply terminals.